



## Media Alert

March 13, 2008

### AMBRIC TO SHOWCASE ITS AM2045 MPPA DEVICE AND PRESENT A PAPER AT SPIE DEFENSE+SECURITY 2008

**WHO:** Ambric<sup>®</sup>, Inc., a fabless semiconductor company shipping the world's first one teraOPS-class Am2000™ chip family of highly scalable, massively parallel processor arrays (MPPAs), using Ambric's award-winning\* structured object programming model tools to make massively parallel software development practical for complex embedded systems, will be exhibiting and delivering a paper at SPIE Defense+Security 2008.

**WHAT:** **Ambric will showcase its new Am2045™ massively parallel processor array (MPPA) device, aDesigner™, a comprehensive software development tool suite, and the Am2045 GT™ development and accelerator board.** The Am2045 device offers leading-edge performance with low power to meet the growing demands of embedded systems in the military and defense industries. The Am2045 is capable of replacing multiple FPGAs and/or DSPs in an embedded system solution, as it has more than 300 processors on a single chip. It delivers over one teraOPS processing power and utilizes just 6 to 12 watts of power, depending on the application. The solution provides significant savings in development cost and time-to-market. Ambric's MPPA device offers the performance capabilities of an FPGA, while eliminating the timing closure problems associated with an FPGA. In addition, Ambric's device offers 6 to 25 times the performance and two-thirds reduction in code when compared with a TI C641x DSP. The Am2045 is well-suited for various military applications including phased-array radar, electro-optical, signal intelligence, and anti-submarine warfare.

Also, **Ambric's Paul Y. Chen will present Paper 6968-67**, which he co-authored with Ambric's Michael Butts and Brad Budlong, entitled "Multi-Mode Sensor Processing on a Dynamically Reconfigurable Massively Parallel Processor Array".

**WHEN:** Ambric will be exhibiting at SPIE from Tues., Mar. 18 through Thurs., Mar. 20 in Booth 54 in the Cypress Ballroom. The Ambric paper will be presented on Tues., Mar. 18 at 6:00 p.m. - 7:30 p.m. in the poster session of the Signal Processing, Sensor Fusion, and Target Recognition Conference, the Palms Ballroom.

**WHERE:** Orlando World Center Marriott Resort and Convention Center, Orlando, Florida.

**EVENT URL:** <http://spie.org/defense-security.xml>

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## About Ambric, Inc.

Ambric is a fabless semiconductor company that is shipping the world's first one teraOPS-class processor and tools that make massively parallel software programming practical for complex, embedded systems and enable price-performance breakthroughs in video processing. The company's highly scalable, massively parallel processor arrays (MPPAs) deliver performance that is more than an order of magnitude greater than high-end DSPs, and four times the video throughput of the fastest quad-core CPUs with 15 times better energy efficiency. The price-performance also exceeds that of FPGAs on complex applications while enabling faster, easier development in software. Ambric products help companies accelerate time-to-market for their solutions while slashing their system development costs.

Established in 2003 and headquartered in Beaverton, Ore., Ambric has received funding from ComVentures, OVP Venture Partners, Northwest Technology Ventures, investment banks, and private investors. Visit <http://www.ambric.com/> for the latest news and information on the company.

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## Editor Notes:

*Ambric and the Ambric logo are registered trademarks, and Am2XXX, aDesigner, and Am2045 GT are trademarks of Ambric, Inc. All other trademarks are the property of their respective owners.*

*Photos of Ambric's massively parallel processor chip, the Am2045, and the Am2045 GT are available from Ambric. Please contact [jean@akipr.com](mailto:jean@akipr.com).*

*\*Ambric was given In-Stat's 2006 Microprocessor Report Analysts' Choice Award for Innovation in February 2007 for its Am2000 family architecture.*