



Enabling Rapid Time to Market for High Performance Solutions



## Corporate Fact Sheet

**Overview:** Ambric is the leader in massively parallel processor arrays (MPPAs), delivering more than one trillion operations per second (TeraOPS) of performance for embedded system designs. Founded in Beaverton, Ore., Ambric launched its first semiconductor chip in 2007, with 336 32-bit processors tied together in a self-synchronizing fabric. The patented BRIC™ architecture scales to tens of thousands of processors using a simple hierarchical object-based software development environment. Ambric's software development tool suite with its practical structural object programming model cuts design development time by more than half. The patented hardware fabric and unique programming model together offer customers a more deterministic, reliable, and faster time-to-market solution.

**Products:** Ambric's array of products include the Am2000 family of silicon devices, tools and libraries, development boards and IP content.

**Target Markets:** Ambric's MPPA devices are targeted at video & image processing, medical, military, wireless and network security markets. These MPPA devices address broad range of applications currently served by high-end DSPs and FPGAs by either replacing or working in conjunction with them. Numerous customers have affirmed Ambric's MPPA devices with shipping products, designs wins and design validations.

**Funding:** Established in 2003 and headquartered in Beaverton, Ore., Ambric has received funding from ComVentures, OVP Venture Partners, Northwest Technology Ventures, Advanced Equities, Inc. (AEI) and private investors.

- Executive Management:**
- Howard Bubb, Chairman and CEO
  - Steve Frison, Vice-President Software Engineering
  - Marc Quattromani, Vice-president IC Engineering
  - Dave Nickel, Vice-President Finance
  - Joe Herbst, Vice-President Marketing
  - Jim Souers, Senior Director, World Wide Sales
  - Mike Butts, Ambric Fellow and Lead Chip Architect

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